

Customer No.: 31561  
Application No.: 10/707,683  
Docket No.: 11846-US-PA

### AMENDMENT

#### To the Claims:

1. (currently amended) A chip package structure, comprising:

a carrier;

a chip, having an active surface with a plurality of bumps thereon, wherein the active surface of the chip is bonded to the carrier using a flip-chip bonding technique so that the chip and the carrier are electrically connected; and

an encapsulating material layer, covering the chip and the carrier and filling the bonding gap between the chip and the carrier, wherein the encapsulating material layer between the chip and the carrier has a first thickness and the encapsulating material layer on the chip has a second thickness such that the second thickness is between 0.5 ~ 2 times the first thickness,

wherein the chip package structure has the only one chip therein and the chip is covered by the encapsulating material layer such that the chip in the chip package structure is enclosed by the encapsulating material layer.

2. (original) The chip package structure of claim 1, wherein maximum diameter of particles constituting the encapsulating material layer is smaller than 0.5 times the first thickness.

3. (original) The chip package structure of claim 1, wherein the package further comprises an array of solder balls attached to a surface of the carrier away from the chip.

4. (original) The chip package structure of claim 1, wherein the package further

Customer No.:31561  
Application No.: 10/707,683  
Docket No.: 11846-US-PA

comprises a passive component attached and electrically to the carrier.

5. (original) The chip package structure of claim 1, wherein the carrier is selected from a group consisting of a package substrate and a lead frame.

6-20 (canceled)